	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	44	(driver$1) same (print$3 near2 board$1 or print$3 near3 circuit$3 or IC$1 near2 board$1 or IC$1 near3 circuit$3) same (equaliz$10)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:56	
2	BRS	L2	589	(driv$3 near2 circuit$3 or driv$3 near3 logic$1 or driv$3 nar2 device$1) same (voltage near3 level$1 with equaliz$10)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:32	
3	BRS	L3	4	(voltage near3 level$1 with equaliz$10) same (print$3 near2 board$1 or print$3 near3 circuit$3 or IC$1 near2 board$1 or IC$1 near3 circuit$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:32	
4	BRS	L4	22	(voltage near3 level$1 with equaliz$10) same (print$3 near2 board$1 or print$3 near3 circuit$3 or IC$1 near2 board$1 or IC$1 near3 circuit$3 or integrated adj2 circuit$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:33	
5	BRS	L5	398	(vmax or vmin or vlow or vhi or upper near2 voltage$1 or low$3 near2 voltage$1 or minimum near2 voltage$1 or maximum near2 voltage$1 or three near3 level$1 or third adj2 level$1 or third adj2 voltage$1) and 2	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:44	

	Error Definition	Err ors
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	L6	117	(vmax or vmin or vlow or vhi or upper near2 voltage$1 or low$3 near2 voltage$1 or minimum near2 voltage$1 or maximum near2 voltage$1 or three near3 level$1 or third adj2 level$1 or third adj2 voltage$1) same 2	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:44	
7	BRS	L7	64	5 and (print$3 near2 board$1 or print$3 near3 circuit$3 or IC$1 near2 board$1 or IC$1 near3 circuit$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:45	
8	BRS	L8	3476	(driver$1 with level$1 or driv$3 near4 multi$3 adj2 level$1 or transistor$1 with level$1) same (equaliz$10 near3 level$1 or adjust$3 near3 level$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:59	
9	BRS	L9	0	8 same reeiver$1	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:58	
10	BRS	L10	103	8 same receiver$1	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:59	

	Error Definition	Errors
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	L11	678	(driver$1 with level$1 or driv$3 near4 multi$3 adj2 level$1 or transistor$1 with level$1) same (equaliz$10 near3 level$1 or equaliz$10 with adjust$3 near3 level$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:59	
12	BRS	L12	15	11 same receiver$1	US-PGPUB; USPAT; EPO; JPO	2005/04/19 14:59	
13	BRS	L13	65	11 and receiver$1	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:15	
14	BRS	L14	1	(driv$3 near3 multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1) with equaliz$10 same (receiv$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:18	
15	BRS	L15	1	(driv$3 near3 multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1) same equaliz$10 same (receiv$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:18	
16	BRS	L16	1	(driv$3 near3 multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1) with equaliz$10	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:19	

	Error Definition	Errors
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
17	BRS	L17	8	(driver$1 or driving or buffer$1 or transisito$1) with (multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1 or voltage$1 near2 level41) with equaliz$10	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:20	
18	BRS	L18	148	(driver$1 or driving or buffer$1 or transisito$1) with (multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1 or voltage$1 near2 level$1) with equaliz$10	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:20	
19	BRS	L19	148	(driver$1 or driving or buffer$1 or transisitoR$1) with (multi$3 adj2 level$1 or driv$3 near3 plural$3 adj2 level$1 or voltage$1 near2 level$1) with equaliz$10	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:21	
20	BRS	L20	119	19 and receiv$3	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:21	
21	BRS	L21	58	20 and (IC$1 or printed near2 circuit$3 or integrated near2 circuit$3)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 15:50	

	Error Definition	Err ors
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
22	BRS	L22	1620	(driv$3 near3 transistor$1 or differential$1 near3 transistor$1 or buffer$1 near3 transistor$1 or logic near3 transistor$1 or combin$10 nea3 transistor$1) with (equaliz$9 near3 voltage$1 or equaliz$10 near3 level$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:07	
23	BRS	L23	45248	receiv$3 with (IC$1 or printed near2 circuit$3 or printed near2 device$1 or printed near2 board$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:08	
24	BRS	L24	45248	23 and 23	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:06	
25	BRS	L25	124	(driv$3 near3 transistor$1 or differential$1 near3 transistor$1 or buffer$1 near3 transistor$1 or logic near3 transistor$1 or combin$10 near3 transistor$1) with (equaliz$9 near3 voltage$1 or equaliz$10 near3 level$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:05	

26	BRS	L27	2	25 and 23	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:06	

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
27	BRS	L26	37	22 and 23	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:06	
28	BRS	L28	124	(driv$3 near3 transistor$1 or differential$1 near3 transistor$1 or buffer$1 near3 transistor$1 or logic near3 transistor$1 or combin$10 near3 transistor$1) with (equaliz$9 near3 voltage$1 or equaliz$10 near3 level$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:07	
29	BRS	L29	31	28 and (IC$1 or printed near2 circuit$3 or printed near2 device$1 or printed near2 board$1)	US-PGPUB; USPAT; EPO; JPO	2005/04/19 16:19	

	Error Definition	Errors
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